

## **AMENDMENTS TO THE SPECIFICATION:**

Please substitute the amended paragraphs as provided below for their respective pending paragraphs. Please note that the page and paragraph number corresponds with that in the specification as filed.

Please add the following paragraph between the paragraph beginning "Figure 1 is an exploded view" and the paragraph beginning "Figure 2 is a side perspective view" on page 4:

Figure 1b is an exploded view of a side perspective view of a further embodiment of the membrane switch circuit layout.

Please add the following paragraph between the paragraph beginning "Figure 2 is a side perspective view" and the paragraph beginning "Figure 3 is a top perspective view" on page 4:

Figure 2b is a side perspective view of the embodiment of the membrane switch circuit layout shown in Figure 1b.

Please replace the description on page 6, lines 9-1 with the following:

As should be obvious to one skilled in the art, it is possible to use the same method of positioning multiple membrane layers over one another with thru-holes through top membrane layers providing electrical connection between different circuit paths to manufacture a membrane switch circuit layout comprising more than two membrane

layers. That is, as shown in Figures 1b and 2b, a three membrane layer membrane switch circuit layout, for example, is manufactured as follows: The top surface of a first membrane layer 10 is printed with a first conductive circuit trace 14. A first adhesive 16 is positioned between the first and second membrane layers 10 and 12. The second membrane layer 12 is printed with a second conductive circuit trace 18 and has thru-holes 20 selectively cut there through and located to connect the first membrane circuit trace 14 with the second membrane circuit trace 18. The thru-holes 20 are press-filled with conductive ink. A second adhesive 16b is positioned between the second and third membrane layers 12 and 12b. The third membrane layer 12b is printed with a third conductive circuit trace 18b and has thru-holes 20b selectively cut there through and located to connect the second membrane circuit trace 18 with the third membrane circuit trace 18b. The thru-holes 20b are press-filled with conductive ink. Thus, for each additional membrane layer, an additional adhesive is applied between the topmost layer and the additional layer, the additional layer is printed with a membrane circuit trace, and the thru-holes are press-filled with conductive ink.